Validation of Interprocedural Optimizations

Amir Pnueli, Anna Zaks
New York University

COCV'08, Budapest, Hungary, April 5th, 2008
Translation Validation

Translation validation frameworks can be categorized by the intended user:

- **Compiler writers** are interested in methods that lead to creation of a self-certifying compiler. The full control over the compiler allows for powerful and efficient techniques.

- **Compiler users** may need to work with an existing compiler and require tools that accommodate minimal compiler cooperation.
  - Pnueli, Siegel, Singerman: Translation validation, 1998
  - Necula: Translation validation for an optimizing compiler, 2000
  - Rival: Symbolic transfer function-based approaches to certified compilation, 2004

Currently, the existing translation validation approaches do not deal with **interprocedural optimizations**. Our work is an extension of the VOC framework to deal with procedural programs.
Overview of VOC

- Each optimized compiler run is followed by a validation pass that generates a proof of translation correctness. The proof is later checked by a third-party validity checker (CVC 3).
- The method does not require a specialized compiler; however, it utilizes compiler debug information to minimize false alarms.
Our Contribution

- Dead Argument Elimination
- Validity Checker
- Common Subexpression Elimination
- Dead Code Elimination
- Code Motion
- Constant Propagation
- Target
- Proof
- Source
- Global Constant Propagation
- Tail Recursion Elimination
- Inlining
- Cloning
- Correct Translation
- Error (false alarms possible)

Validation of Interprocedural Optimizations
Roadmap

- **Transition Graphs** as our formal model of programs with procedures.

- The notion of **Correct Translation**.

- The **Validation Algorithm** that constructs a proof of Correct Translation in presence of interprocedural optimizations like:
  - global constant propagation,
  - inlining,
  - tail recursion elimination,
  - dead argument elimination,
  - and others.

- **Generation of inductive assertion network**, required to handle global constant propagation.
Transition Graphs

Each procedure is represented by a transition graph.

Nodes of the graph $N$ must include all the program cut-points.

The nodes are connected by directed edges labeled by instructions:

- A Guarded assignment is an instruction of the form $c \rightarrow \overrightarrow{\vec{u} := \vec{E}(\vec{y})}$, where $c$ is a boolean expression.

- Read and write instructions are denoted by $\text{read}(\vec{u})$ and $\text{write}(\vec{u})$.

- Procedure call instruction $f(\vec{E}(\vec{y}); \vec{u})$ denotes a call to procedure $f(\vec{x}_f; \& \vec{z}_f)$, passing input parameters $\vec{E}(\vec{y})$ by value and output parameters $\vec{u}$ by reference.
Factorial Example

MAIN()
{ On input A, outputs: (3 * A)! + 5 }

read (A)  (C, B) := (5, 1)  MFAC (3 * A, B)  write (B + C)

MFAC (X; &Z)
{ Computes in Z: Z * X! }

(X > 1) → Z := Z * X  MFAC (X − 1, Z)  (X ≤ 1)?
Correct Translation

• An observation of a program $\mathcal{A}$ is obtained from a program computation by setting to $T$ the transitions and data that do not participate in a read or write instructions.

For example, below is an observation of a program that reads two numbers and then writes out their product:

\[
T \xrightarrow{\text{read}} (5, 22) \xrightarrow{T} T \xrightarrow{T} T \xrightarrow{T} T \xrightarrow{T} (110) \xrightarrow{\text{write}} T
\]

• Observations $o'$ and $o$ are stuttering equivalent if they differ from each other by finite sequences of pairs $T \xrightarrow{T}$ or $T \xrightarrow{T}$.

For instance, the observation above is stuttering equivalent to the following:

\[
T \xrightarrow{\text{read}} (5, 22) \xrightarrow{T} T \xrightarrow{T} T \xrightarrow{T} (110) \xrightarrow{\text{write}} T
\]

• Program $\mathcal{T}$ is a correct translation (refinement) of program $\mathcal{S}$ if, for every observation $o_{\mathcal{T}} \in \text{Obs}(\mathcal{T})$, there exists a stuttering equivalent observation $o_{\mathcal{S}} \in \text{Obs}(\mathcal{S})$. 
Translation Validation Algorithm

Goal: Given two programs $S$ and $T$, construct a proof showing that the target program $T$ is a correct translation of the source $S$. 
Factorial Example

MAIN()
1 read (A) 2 (C, B) := (5, 1) 3 MFAC (3 * A, B) 4 write (B + C) 5

S
6 (X > 1) → Z := Z * X 7 MFAC (X - 1, Z) 8

MFAC (X; &Z)

T
6 x := x - 1 7 (x > 1) → z := z * x 8

Mfac (x; &z)

main()
1 read (a) 2 (k, b) := (3 * a, 1) 3 mfac (k, b) 4 write (b + 5) 5

Validation of Interprocedural Optimizations
Translation Validation Algorithm

Goal: Given two programs $S$ and $T$, construct a proof showing that the target program $T$ is a correct translation of the source $S$.

1. Establish control mapping $\kappa : N^T \rightarrow N^S$ mapping the target nodes to the source nodes, such that $r$ is the initial location of $T$ iff $\kappa(r)$ is the initial location of $S$.
   - The mapping is total.
   - Optimizations such as inlining result in a non-injective (many-to-one) control mapping.
   - $\kappa$ will not be surjective (onto) if dead code elimination removes a loop.

The compiler debug information is used to establish the correspondence between the source and target nodes.
Factorial Example: Control Mapping

\[
\begin{align*}
S & \\
\text{MAIN}() & \\
1 & \text{read} (A) \quad 2 \quad (C, B) := (5, 1) \\
3 & \text{MFAC} (3 \times A, B) \\
4 & \text{write} (B + C) \\
5 & \\
\text{MFAC} (X; & Z) & \\
6 & \quad (X > 1) \rightarrow Z := Z \times X \\
7 & \quad (X \leq 1) ? \\
8 & \quad \text{MFAC} (X - 1, Z) \\
\text{mfac} (x; & z) & \\
6 & \quad (x > 1) \rightarrow z := z \times x \\
7 & \quad (x \leq 1) ? \\
8 & \\
\text{main()} & \\
1 & \text{read} (a) \\
2 & \quad (k, b) := (3 \times a, 1) \\
3 & \quad \text{mfac} (k, b) \\
4 & \quad \text{write} (b + 5) \\
5 & \\
\end{align*}
\]
Translation Validation Algorithm

Goal: Given two programs $S$ and $T$, construct a proof showing that the target program $T$ is a correct translation of the source $S$.

1. Establish control mapping $\kappa : N^T \rightarrow N^S$ mapping the target nodes to the source nodes.

2. Form data abstraction $D = \{\alpha_0, \ldots, \alpha_{|N^T|}\}$ by defining each $\alpha_l(V^S; V^T)$, for target location $l \in N^T$, as a conjunction of equalities of the form $E(V^S) = E(V^T)$. Note that implicitly, $\alpha_l$ is associated with a pair of target and source locations $l$ and $\kappa(l)$. Thus, the data abstraction relates values of target variables to those of source variables.

Following [VOC 2003], the data abstraction is based on the specialized data flow analysis and the mapping between the source and target variables available from the compiler debug information.
Factorial Example: Data Abstraction

\[ mfac \left( k, b \right) = \begin{cases} \text{true} & \text{if } k = 1 \\ \left( A = a \right) & \text{if } k > 1 \\ \left( A \cdot 3 = k \right) \land \left( B = b \right) \land \left( C = 5 \right) & \text{if } k > 1 \\ \left( A \cdot 3 = k \right) \land \left( B = b \right) \land \left( C = 5 \right) & \text{if } k > 1 \\ \text{true} & \end{cases} \]

\[ main() \]
1. \textit{read} \( A \)
2. \((C, B) := (5, 1)\)
3. \(MFAC(3 \cdot A, B)\)
4. \textit{write} \((B + C)\)

\[ S \]

\[ MFAC(X; \& Z) \]
6. \((X > 1) \rightarrow Z := Z \cdot X\)
7. \(MFAC(X - 1, Z)\)

\[ T \]

\[ mfac(x; \& z) \]
6. \((x > 1) \rightarrow z := z \cdot x\)
7. \((x \leq 1)\)?
Translation Validation Algorithm

Goal: Given two programs $S$ and $T$, construct a proof showing that the target program $T$ is a correct translation of the source $S$.

1. Establish control mapping $\kappa : N^T \rightarrow N^S$ mapping the target nodes to the source nodes.

2. Form data abstraction $D = \{\alpha_0, \ldots, \alpha_{|N^T|}\}$ by defining each $\alpha_l(V^S; V^T)$, for target location $l \in N^T$, as a conjunction of equalities of the form $E(V^S) = E(V^T)$. The data abstraction must be valid at the initial location of $T$, $\alpha_r = true$.

3. Generate translation verification conditions and place them in a set $\mathcal{VC}$, which forms an inductive proof of correct translation.
   - Form the initial verification condition: $true \rightarrow \alpha_r$
   - Form translation verification conditions for every edge of the target program.
I/O Verification Conditions

Read: For $e^T = (i, j)$ and $e^S = (\kappa(i), \kappa(j))$, labeled $\text{read}(\vec{u}^T)$ and $\text{read}(\vec{u}^S)$:

$$\alpha_i \land (\vec{u}^T = \vec{u}^S) \rightarrow \alpha_j$$

Write: For $e^T = (i, j)$ and $e^S = (\kappa(i), \kappa(j))$, labeled by $\text{write}(\vec{E}^T)$ and $\text{write}(\vec{E}^S)$:

$$\alpha_i \rightarrow \alpha_j \land (\vec{E}^T = \vec{E}^S)$$
Factorial Example: Read Verification Condition

\[ \alpha_1 \land (A = a) \rightarrow \alpha_2 \quad \iff \quad true \land (A = a) \rightarrow (A = a) \]
Factorial Example: Write Verification Condition

\[ \alpha_4 \rightarrow \alpha_5 \land (B + C = b + 5) \]
\[ \iff \]
\[ (A \times 3 = k \land B = b \land C = 5) \]
\[ \rightarrow true \land (B + C = b + 5) \]
Assignment Verification Condition

Assignment Verification Condition is

- left as an exercise for the reader.
- very similar to the condition presented in [VOC 2003].
Call Verification Conditions are generated, when both $e^T$ and $e^S$ are call edges.

$$F^S(\vec{x}_F; \& \vec{z}_F)$$

$$G^S(...)$$

$$e^S \rightarrow \kappa(i) \quad \kappa(j)$$

$$f^T(\vec{x}_F; \& \vec{z}_F)$$

$$\alpha_r((\vec{x}_F, \vec{z}_F); (\vec{x}_f, \vec{z}_f))$$

$$\alpha_t(\vec{z}_F; \vec{z}_f)$$

$$\kappa(r) \quad \kappa(t)$$

$$g^T(...)$$

$$\alpha_i(\vec{y}_G; \vec{y}_g) \rightarrow \alpha_j(\vec{y}_G; \vec{y}_g)$$

$$\alpha_i(\vec{y}_G; \vec{y}_g)$$

$$\rightarrow \alpha_j(\vec{y}_G; \vec{y}_g) \quad [\vec{u}_G \mapsto \vec{z}_F; \vec{u}_g \mapsto \vec{z}_f]$$

$$\kappa(i) \quad \kappa(j)$$

$$\kappa(r)$$

$$\kappa(t)$$

$$r$$

$$t$$

$$i$$

$$j$$

$$\vec{E}_G$$

$$\vec{u}_G$$

$$\vec{E}_g$$

$$\vec{u}_g$$

where $\alpha_j(\vec{y}_G; \vec{y}_g) \quad [\vec{u}_G \mapsto \vec{z}_F; \vec{u}_g \mapsto \vec{z}_f]$ is obtained from $\alpha_j(\vec{y}_G; \vec{y}_g)$ by replacing variables in $\vec{u}_G$ by the corresponding variables in $\vec{z}_F$ and variables in $\vec{u}_g$ by the variables in $\vec{z}_f$. 

\[\forall C_{call} : \quad \alpha_i(\vec{y}_G; \vec{y}_g) \quad \rightarrow \quad \alpha_r((\vec{E}_G, \vec{u}_G); (\vec{E}_g, \vec{u}_g))\]

\[\forall C_{ret} : \quad \alpha_i(\vec{y}_G; \vec{y}_g) \land \alpha_t(\vec{z}_F; \vec{z}_f) \quad \rightarrow \quad \alpha_j(\vec{y}_G; \vec{y}_g) \quad [\vec{u}_G \mapsto \vec{z}_F; \vec{u}_g \mapsto \vec{z}_f]\]
Factorial Example: \( \nu C_{\text{call}} \)

\[ \text{MAIN}() \]

1. \textit{read} \((A)\)  
2. \((C, B) := (5, 1)\)  
3. \textit{MFAC} \((3 \ast A, B)\)  
4. \textit{write} \((B + C)\)  
5.   

\[ \text{S} \]

6. \((X > 1) \rightarrow Z := Z \ast X\)  
7. \textit{MFAC} \((X - 1, Z)\)  
8.   
9. \((X \leq 1)\)?  

\[ \text{T} \]

6. \((x > 1) \rightarrow z := z \ast x\)  
7.   
8.   
9. \((x \leq 1)\)?  

\[ \alpha_3 : (A \ast 3 = k) \land (B = b) \land (C = 5) \]
\[ \alpha_6 : (X = x) \land (Z = z) \]
\[ \alpha_3 \rightarrow \alpha_6 \ [(X, Z) \mapsto (A \ast 3, B); \ (x, z) \mapsto (k, b)] \]
\[ (A \ast 3 = k \land B = b \land C = 5) \rightarrow (A \ast 3 = k) \land (B = b) \]

\[ \text{main()} \]

1. \textit{read} \((a)\)  
2. \((k, b) := (3 \ast a, 1)\)  
3. \textit{mfac} \((k, b)\)  
4. \textit{write} \((b + 5)\)  
5.   

Validation of Interprocedural Optimizations
**Factorial Example:** $\nu C_{\text{return}}$

**MAIN()**

1. $\text{read}(A)$
2. $(C, B) := (5, 1)$
3. $\text{MFAC}(3 \times A, B)$
4. $\text{write}(B + C)$

**S**

**MFAC $(X; & Z)$**

6. $(X > 1) \rightarrow Z := Z \times X$
7. $\text{MFAC}(X - 1, Z)$

$(X \leq 1)$?

**T**

**mfac $(x; & z)$**

6. $(x > 1) \rightarrow z := z \times x$
7. $(x \leq 1)$?

**main()**

1. $\text{read}(a)$
2. $(k, b) := (3 \times a, 1)$
3. $\text{mfac}(k, b)$
4. $\text{write}(b + 5)$

Validation of Interprocedural Optimizations
Inlining and Tail-Recursion Elimination (TRE) introduce situations in which the source code contains a call edge that corresponds to a subgraph in the target.

In this case, we prove the translation by “stepping into” the procedure call on the source.
TRE Verification Conditions

**Definition**: A call edge \((i, t)\) of procedure \(G(\vec{x}; \& \vec{z})\) is a TRE candidate if it is labeled by \(G(\vec{E}; \vec{z})\) and \(t\) is the exit node of the procedure.

\[
\begin{align*}
G^S(\vec{x}_G; \& \vec{z}_G) & \quad \kappa(r) \\
G^S(\vec{E}_G; \vec{z}_G) & \quad e^S \\
G^S(\vec{x}_G; \& \vec{z}_G) & \quad \kappa(i) \\
G^S(\vec{E}_G; \vec{z}_G) & \quad \kappa(t)
\end{align*}
\]

\[
\begin{align*}
g^T(\vec{x}_g; \& \vec{z}_g) & \quad r \\
\alpha_r((\vec{x}_G, \vec{z}_G); \vec{y}_g) & \quad e^T \\
g^T(\vec{x}_g; \& \vec{z}_g) & \quad i \\
\alpha_i(\vec{y}_G; \vec{y}_g) & \\
\alpha_i(\vec{y}_G; \vec{y}_g) & \quad \alpha_t(\vec{z}_G; \vec{z}_g) \\
\alpha_t(\vec{z}_G; \vec{z}_g) & \quad t
\end{align*}
\]

\[
\forall C_{call}: \alpha_i(\vec{y}_G; \vec{y}_g) \rightarrow \alpha_r((\vec{E}_G, \vec{z}_G); \vec{E}_g)
\]
TRE Verification Conditions: Justification

- **Step in:** When $S$ steps deeper into the recursion level and $T$ steps deeper into the iteration level:
  - The validity of the *data abstraction* is guaranteed by the TRE Verification Condition.

- **Step out:** After both programs have reached the full recursion (iteration) depth, and $S$ executes the return transitions:
  - The *data abstraction* is preserved by the returns since the formal parameters passed by reference: $\vec{z}$, are used as the actual parameters in the tail call.
  - The observations of $S$ and $T$ are *stuttering equivalent* since only a finite number of return transitions is executed.
Factorial Example: TRE Verification Condition

**MAIN()**

1. \( \text{read}(A) \)  
2. \( (C, B) := (5, 1) \)  
3. \( \text{MFAC}(3 \ast A, B) \)  
4. \( \text{write}(B + C) \)  

**S**

5. \( (X > 1) \rightarrow Z := Z \ast X \)  
6. \( \text{MFAC}(X; & Z) \)  
7. \( \text{MFAC}(X - 1, Z) \)  
8. \( (X \leq 1)? \)

**MFAC(X; & Z)**

6. \( (X > 1) \rightarrow Z := Z \ast X \)  
7. \( \text{MFAC}(X - 1, Z) \)  
8. \( (X \leq 1)? \)

**α₇**

\( (X = x) \land (Z = z) \)  
\( \alpha₆ : (X = x) \land (Z = z) \)

\( \alpha₇ \rightarrow \alpha₆ [X \leftrightarrow X - 1; x \leftrightarrow x - 1] \)

\( (X = x) \land (Z = z) \rightarrow (X - 1 = x - 1) \land (Z = z) \)

**mfac(x; & z)**

6. \( (x > 1) \rightarrow z := z \ast x \)  
7. \( x := x - 1 \)  
8. \( (x \leq 1)? \)

**T**

9. \( \text{read}(a) \)  
10. \( (k, b) := (3 \ast a, 1) \)  
11. \( \text{mfac}(k, b) \)  
12. \( \text{write}(b + 5) \)
Example of Constant Propagation: Source

```
main()

0

  y_m := 8

1

  foo(x_m, 8, y_m, z_m)

5

  write(z_m)

6

  foo((x_m + 6), 8, y_m, z_m)

7

foo(x, c, &y, &z)

2

  (x ≥ 0) → [(x, z) := (x - 2, y)]

3

  (x < 0) → [z := c]

4

  (x = 0) → [y := x]  foo(x, c, y, z)

main

foo(x_m, 8, y_m, z_m)

ym := 8

write(z_m)
```

Validation of Interprocedural Optimizations
Example of Constant Propagation: Target

\[ \text{main()} \]

0
\[
y_m := 8
\]
1
\[
\text{foo}(x_m, 8, y_m, z_m)
\]
5
\[
\text{write}(8)
\]
6
\[
\text{foo}((x_m + 6), 8, y_m, z_m)
\]
7

\[ \text{foo}(x, c, &y, &z) \]

2
\[
(x \geq 0) \rightarrow [(x, z) := (x - 2, y)]
\]
3
\[
(x < 0) \rightarrow [z := c]
\]
4
\[
(x = 0) \rightarrow [y := x]
\]
\[
\text{foo}(x, c, y, z)
\]
Inductive Assertion Network for Global Constant Propagation

In order to prove translation in presence of interprocedural constant propagation, the verification conditions have to be strengthened with auxiliary assertion network that:

- is strong enough; for example, it allows to prove the translation verification condition associated with the write transitions from \( l_5 \) to \( l_6 \):

\[
\varphi_5 \land \alpha_5 \rightarrow \alpha_6 \land (z_m = 8)
\]

- is inductive; thus, it is self-sufficient for proving that all its assertions are invariants.
First Attempt

On the first thought, any precise solution to the interprocedural constant propagation problem should suffice: $\varphi_l$ should be extended with conjunct $x = 5$ if $x$ always evaluates to constant 5 at location $l$.

The resulting assertion network:

- is strong enough to prove translation but
- may not be inductive.
Back to our Example

For example, the return verification condition for call edge \((1, 5)\) does not hold:

\[
\forall C_{\text{ret}}: \quad \varphi_1 \land \varphi_4 \rightarrow \varphi_5[z_m \mapsto z] \iff
y_m = 8 \land c = 8 \rightarrow z = 8
\]
Solution

• Fortunately, the algorithm presented in *Precise Interprocedural Dataflow Analysis with Applications to Constant Propagation* by M. Sagiv, T. Reps, and S. Horwitz not only provides a solution to the constant propagation problem, but also finds a set of environment transformers. We are using this additional information to strengthen our network so that it would be inductive.

• The environment transformers are represented as a set of functions, where each function $f_{v,v'}$ captures the effect that the value of variable $v$ in the argument environment has on the value of $v'$ in the result environment.

• They relate the values of variables at the beginning of each procedure to the values of the variables at each procedure location.
The list of environment transformers computed for procedure \textit{foo}:

\[
F_{(2,2)} = \{ f_{x,x} = \lambda l.l, f_{c,c} = \lambda l.l, f_{y,y} = \lambda l.l, f_{z,z} = \lambda l.l \} \\
F_{(2,3)} = \{ f_{c,c} = \lambda l.l, f_{y,y} = \lambda l.l, f_{y,z} = \lambda l.l \} \\
F_{(2,4)} = \{ f_{c,c} = \lambda l.l, f_{c,z} = \lambda l.l, f_{y,z} = \lambda l.l \}
\]

Validation of Interprocedural Optimizations
Let's show that the return verification condition for call edge \( (1, 5) \) holds:

\[
\varphi_{\text{ret}}: \varphi_1 \land \varphi_4[(C, Y) \mapsto (8, y_m)] \land y_m = 8 \land c = 8 \land (z = 8 \lor z = y_m) \land c = 8 \rightarrow \varphi_5[z_m \mapsto z] \iff z = 8
\]
Conclusion and Future Work

Completeness:
- Since the approach deals with infinite state systems, we cannot hope to have a complete method for proving translation without imposing any restrictions on the transformer.
- However, because the focus is only on compiler optimizations, the number of false alarms can be drastically minimized or even eliminated.

Implementation:
We are currently developing a tool that verifies the optimizations performed by LLVM compiler and uses CVC3 as the back-end theorem prover.

Extensions to the framework:
- aliasing (in the interprocedural context),
- dynamic memory allocation,
- exceptions.