1. Problem 3.6.a (pages 291 and 292 of the text) with following additional assumptions:

   I. Assume that a queued instruction in a reservation station may execute in the same clock cycle that the previous instruction writes to the CDB.

   II. Also assume that a data dependent instruction begins to execute in the cycle after the data value is broadcasted on the CDB.

Note that for this assignment you have to do only part (a) of the question.